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PATENT *mdj*  
7.3.02

005586-20026 (81784.0018)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Akira TSUKIHASI

Serial No: 09/476,862

Filed: January 3, 2000

For: DATA PROCESSING CIRCUIT FOR  
TEMPORARILY SUSPENDING  
DATA RECORDING ONTO A DISK  
(Amended)

Art Unit: 2653

Examiner: Gautam Patel

PRELIMINARY AMENDMENT

Box CPA  
Commissioner for Patents  
Washington, D.C. 20231

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JUL 02 2002

Technology Center 2600

Dear Sir:

Prior to examination of the above-identified Continued Prosecution  
Application (CPA), please amend the application as follows:

IN THE CLAIMS:

✓ ✓  
Cancel claims 1 and 2, without prejudice.

Please rewrite claim 3 as follows:

*5/5*  
*3/1*  
3. (Twice Amended) A recording data processing circuit for processing  
received data sent at a slower data transmission speed than a data processing speed  
at which to write recording data onto a disk, comprising:  
a buffer memory for temporarily storing the received data;  
a data processing circuit for preparing the recording data to record  
onto the disk, based on the received data read from the buffer memory;